

Fig.1

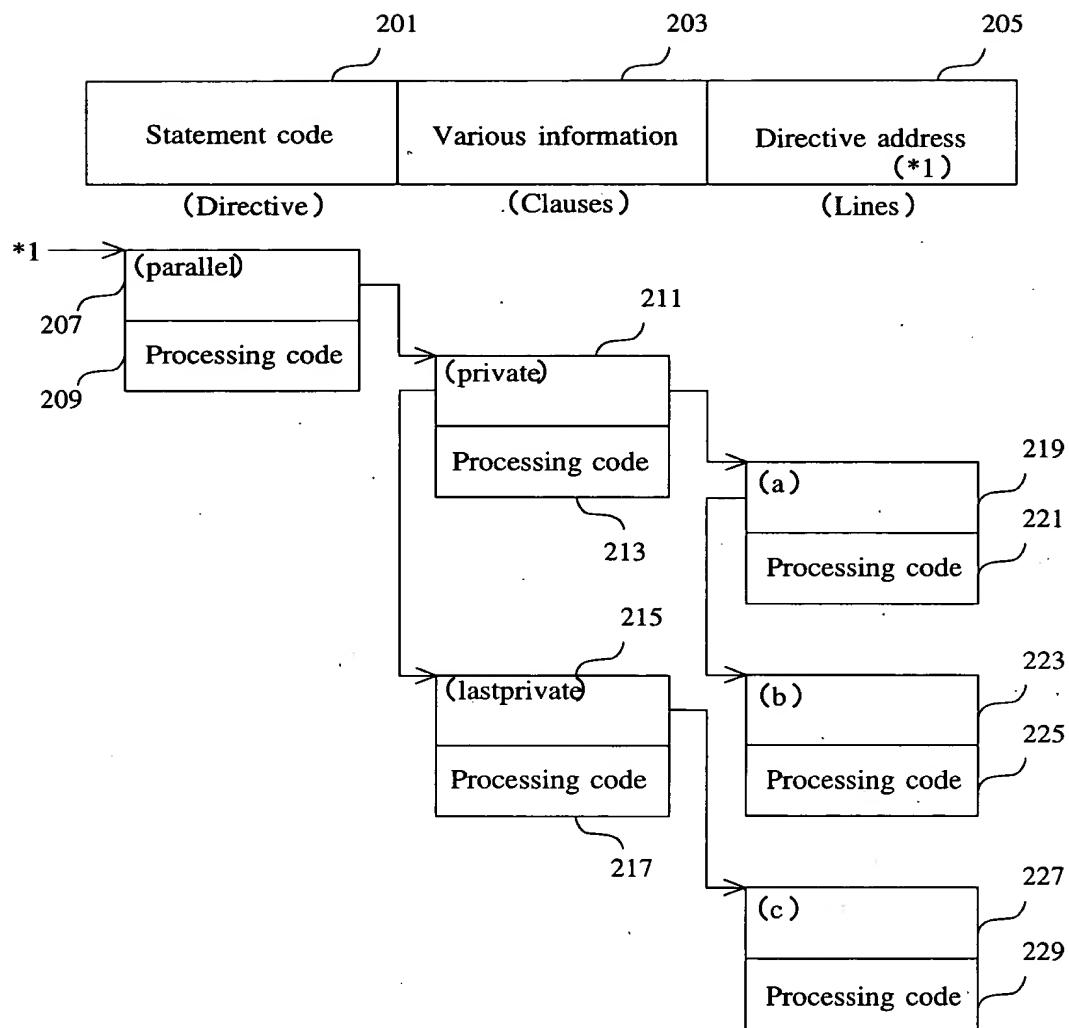


Fig.2

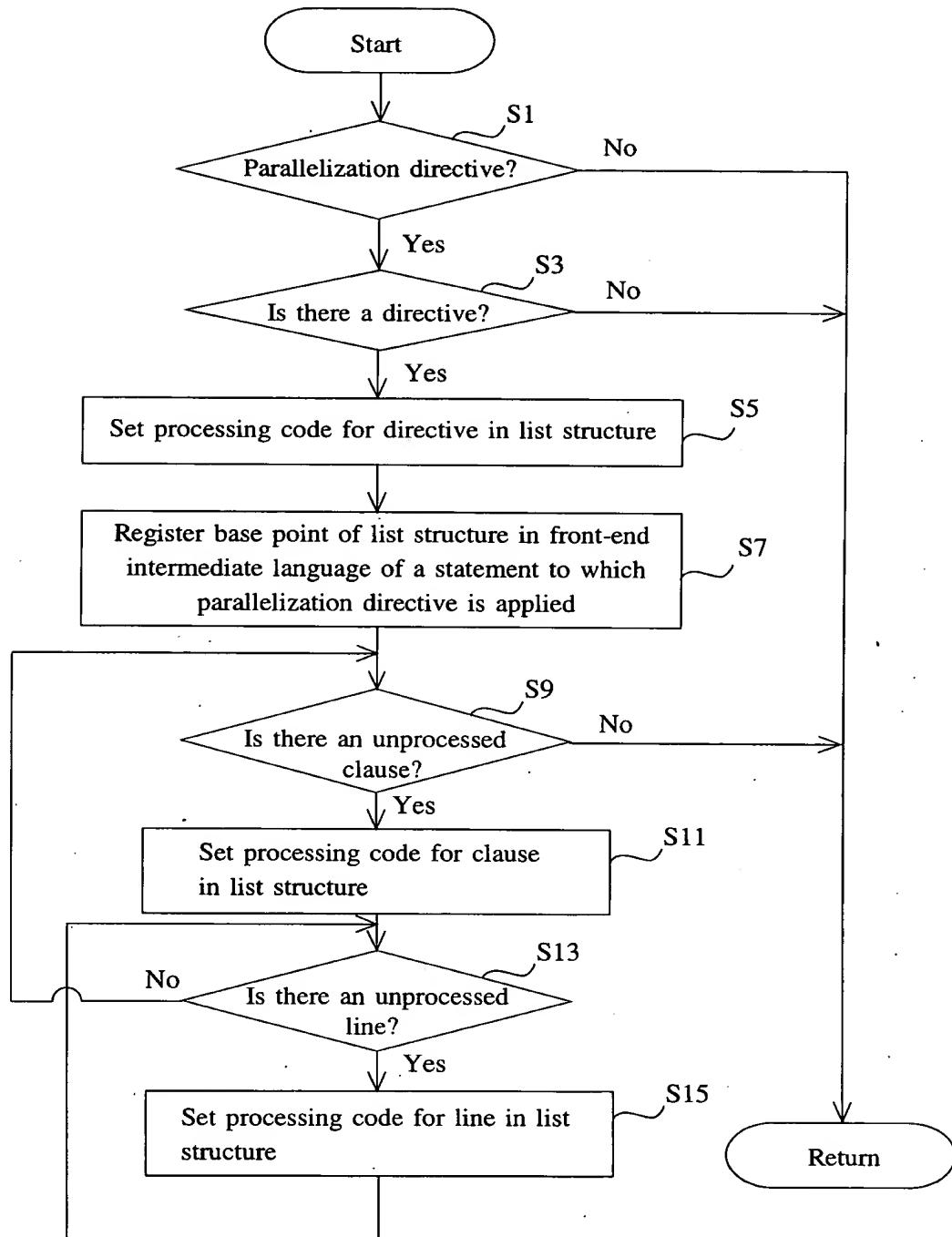


Fig.3

Processing Processing code	Processing 1	Processing 2	-----
405 parallel	a1	b1	-----
407 private	a2	b2	-----
409 variable	a3	b3	-----
411 lastprivate	a4	b4	-----
.....	.....	.....	.....

Fig.4

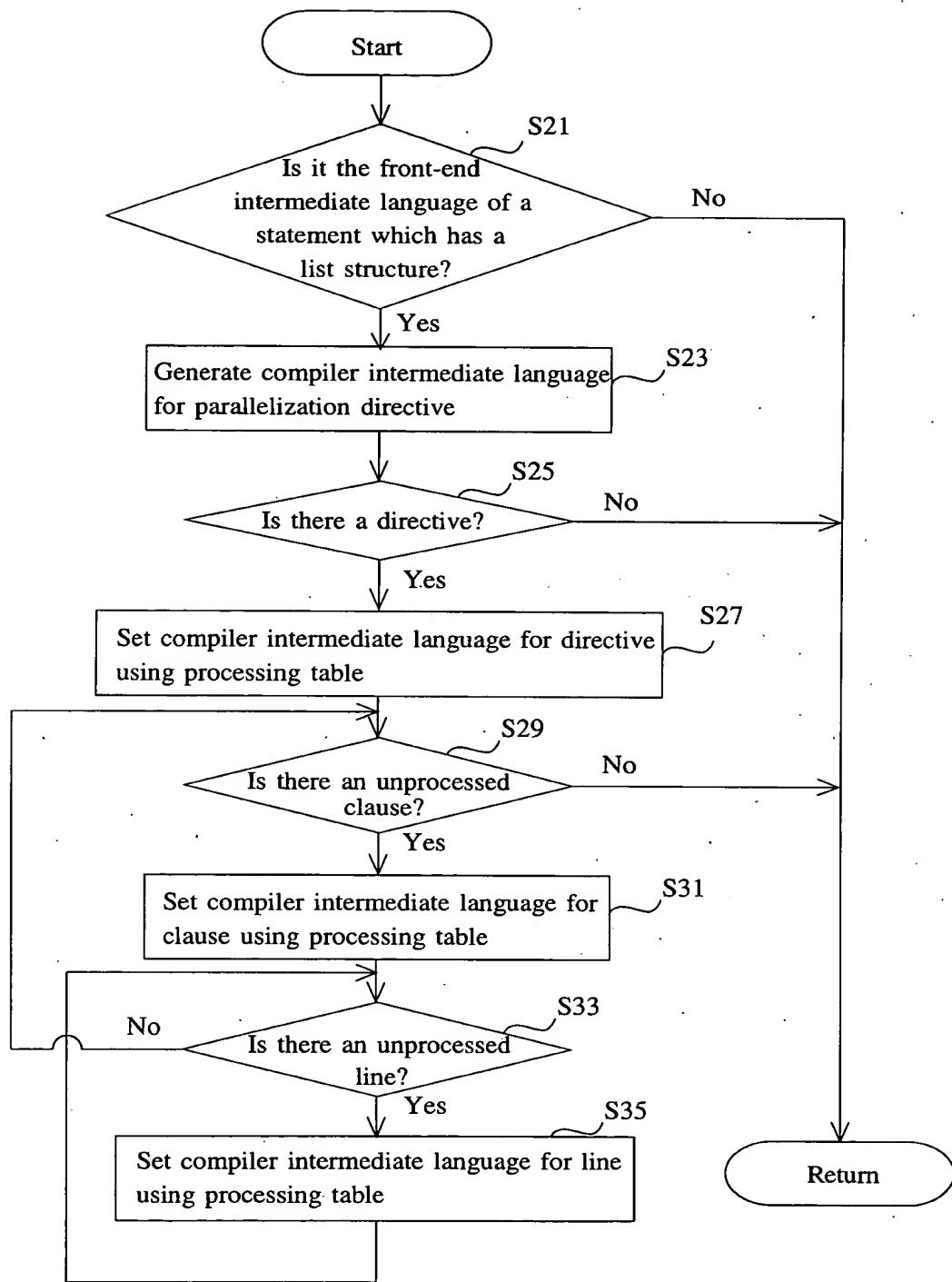


Fig.5